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SEMICONDUCTOR DEVICE FUNDAMENTALS

SEMICONDUCTOR DEVICE FUNDAMENTALS Robert F Pierret School of Electrical and Computer Engineering Purdue University Addison Wesley Longman Reading, Massachusetts • Menlo Park, California • New York

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ECE 4214 SEMICONDUCTOR DEVICE FUNDAMENTALS

*S Wang, Fundamentals of Semiconductor Theory and Device Physics, Englewood Cliffs, NJ: Prentice Hall, 1989 Robert Pierret, "Semiconductor Device Fundamentals", 1st ndor 2 ed [1996, Addison-Wesley or 2002, McGraw-Hill] IV EDUCATIONAL OBJECTIVES

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Robert F Pierret, Semiconductor Device Fundamentals, (Addison-Wesley, 1996) Additional References: Semiconductor fundamentals Crystal structures Chapter 1 Energy bands, electrons and holes, doping Chapter 2 Transport properties Chapter 3 pn Junctions

Electronics B - MIT OpenCourseWare

Cit s, Spring 2007 MIT Op JV: 2372J/6777J Spring 2007, Lecture 7E - 5 Razeghi, Fundamentals of Solid State Engineering > The highest normally filled set of electronic states is the valence band > The lowest normally empty set of electronic states is the conduction band > An energy gap separates these states > At T=0 K, all the valence band states are filled

ECE606: Solid State Devices Lecture 1 - Purdue Engineering

ECE606: Solid State Devices Lecture 1 Gerhard Klimeck gekco@purdueedu Klimeck -ECE606 Fall 2012 -notes adopted from Alam Course Information Books • Advanced Semiconductor Fundamentals (QM, SM, Transport) first 5 weeks • Semiconductor Device Fundamentals (Diode, Bipolar, MOSFET) Weeks 6-15 HW/Exams

Investigation of Power Semiconductor Devices for High ...

Investigation of Power Semiconductor Devices for High Frequency High Density Power Converters Hongfang Wang ABSTRACT The next generation of power converters not only must meet the characteristics demanded by the load, but also has to meet some specific requirements like limited space and high ambient temperature etc

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I. ECE 4214 SEMICONDUCTOR DEVICE FUNDAMENTALS

- Determine alignment of metal -semiconductor band diagrams and identify whether junction is Ohmic or Schottky
- Design a bipolar transistor, metal-oxide-semiconductor and/or a fieldeffect transistor that meet specific performance criteria through the selection of the appropriate semiconductor material(s), doping, and device dimensions II

Section 12: Intro to Devices - University of California ...

Section 12: Intro to Devices Robert F Pierret, Semiconductor Device Fundamentals EE143 - Ali Javey Bond Model of Electrons and Holes • When an electric field is applied to a semiconductor, mobile carriers will be accelerated by the electrostatic force This